

(A): Amendments to the Title and the specification:

Amend the Title to read:

Amend the second paragraph on page 1 to read as follows:

Related Applications:

This invention is related to an application entitled:
Multiprocessor computer system having multiple coherency regions
and software process migration between coherency regions without
cache purges ~~mode changes and a changing mode cache state (which~~
~~uses an additional cache state to eliminate the need to purge~~
~~caches when changing the mode bits)~~, USSN 10/603,252 ~~10/603,251~~,
filed June 25, 2003, filed concurrently herewith and assigned to
the same assignee.